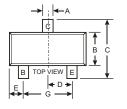
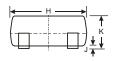


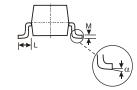
PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction.
- Complementary NPN Type Available(MMBTA42).
- Ideal for Medium Power Amplification and Switching.
- Marking Code:2D







SOT-23						
Dim	Min	Max				
Α	0.37	0.51				
В	1.20	1.40				
С	2.30	2.50				
D	0.89	1.03				
Е	0.45	0.60				
G	1.78	2.05				
Н	2.80	3.00				
J	0.013	0.10				
K	0.903	1.10				
L	0.45	0.61				
M	0.085	0.180				
α	0°	8°				
All Dimensions in mm						

Maximum Ratings @ T_A = 25°C unless otherwise specified

Parameter	Symbol	Value	Unit
Collector Base Voltage	V _{CBO}	-300	V
Collector Emitter Voltage	V_{CEO}	-300	V
Emitter Base Voltage	V _{EBO}	-5	V
Collector Current	I _C	500	mA
Power Dissipation	P _d	300	mW
Junction to Ambient	$R_{\theta JA}$	417	°C/W
Junction Temperature	Tj	150	°C
Storage Temperature Range	T _{Stg}	- 55 to + 150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

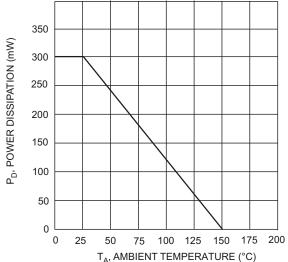
Parameter		Min.	Max.	Unit
DC Current Gain at -V _{CE} = 10 V, -I _C = 1 mA at -V _{CE} = 10 V, -I _C = 10 mA at -V _{CE} = 10 V, -I _C = 30 mA	h _{FE} h _{FE}	25 80 25	- 200 -	- - -
Collector Base Cutoff Current at -V _{CB} = 200 V	-I _{CBO}	-	0.25	μA
Emitter Base Cutoff Current at -V _{EB} = 3 V	-I _{EBO}	-	0.1	μA
Collector Base Breakdown Voltage at -I _C = 100 μA	-V _{(BR)CBO}	300	-	V
Collector Emitter Breakdown Voltage at -I _C = 1 mA	-V _{(BR)CEO}	300	-	V
Emitter Base Breakdown Voltage at $-I_E = 100 \mu A$	-V _{(BR)EBO}	5	-	V
Collector Emitter Saturation Voltage at -I _C = 20 mA, -I _B = 2 mA	-V _{CE(sat)}	-	0.5	V
Base Emitter Saturation Voltage at $-I_C = 20$ mA, $-I_B = 2$ mA	-V _{BE(sat)}	-	0.9	V
Gain Bandwidth Product at $-V_{CE} = 20 \text{ V}$, $-I_C = 10 \text{ mA}$, $f = 100 \text{ MHz}$	f⊤	50	-	MHz
Collector Output Capacitance at -V _{CB} = 20 V, f = 1 MHz	C _{ob}	-	6	pF



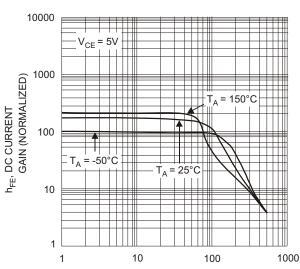


PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR

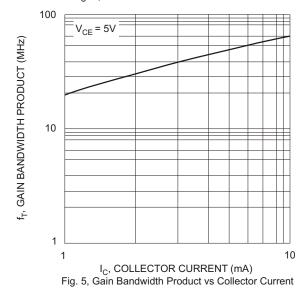
TYPICAL TRANSIENT CHARACTERISTICS



T_A, AMBIENT TEMPERATURE (°C) Fig. 1, Max Power Dissipation vs Ambient Temperature



 $\rm I_{C},$ COLLECTOR CURRENT (mA) Fig. 3, DC Current Gain vs Collector Current



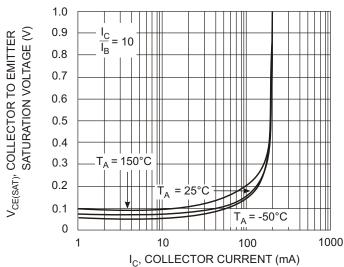


Fig. 2, Collector Emitter Saturation Voltage vs. Collector Current

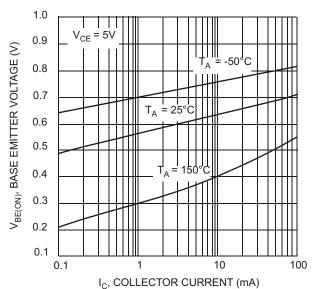


Fig. 4, Base Emitter Voltage vs Collector Current

2/3 http://www.hc-semi.com





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