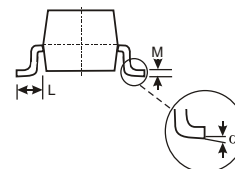
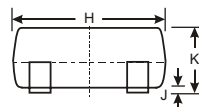
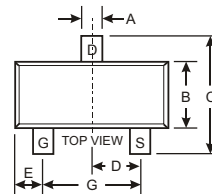


Features

- High power and current handing capability.
- Lead free product is acquired.
- Super high density cell design for extremely low $R_{DS(ON)}$.
- Exceptional on-resistance and maximum DC current capability.
- We declare that the material of product compliance with RoHS requirements.



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°
All Dimensions in mm		

APPLICATIONS

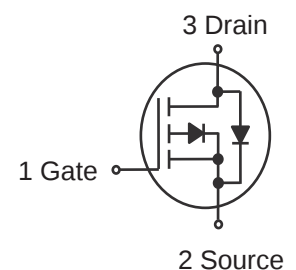
- Power Management in Notebook.
- Portable equipment.
- Battery powered system.
- Load switch.
- Marking Code:3407 OR A79T.

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	-4.3	A
Peak Drain Current ¹⁾	I_{DM}	-20	A
Power Dissipation	P_{tot}	1.5	W
Thermal Resistance from Junction to Ambient (PCB mounted) ²⁾	$R_{\theta JA}$	83	$^\circ\text{C/W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 150	$^\circ\text{C}$

¹⁾ Repetitive Rating: Pulse width limited by the Maximum junction temperature.

²⁾ 1 in² 2oz Cu PCB board.



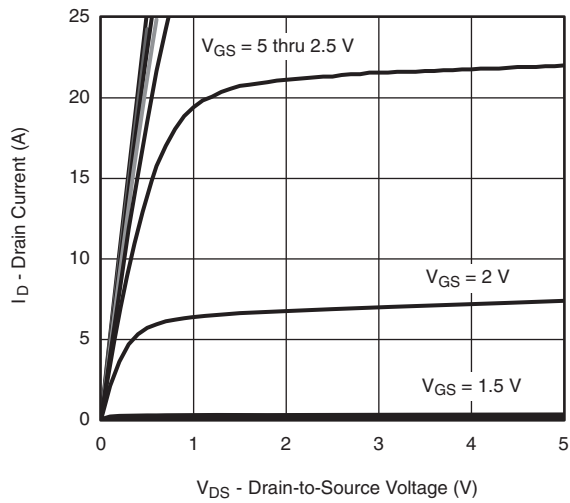
Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} = 0V, I _D =-250μA	-30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =-24V, V _{GS} = 0V			-1	μA
Gate-source leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			±100	nA
Drain-source on-resistance (note 1)	R _{DS(on)}	V _{GS} =-10V, I _D =-4.3A		45	60	mΩ
		V _{GS} =-4.5V, I _D =-3.5A		62	85	mΩ
Forward tranconductance (note 1)	g _{FS}	V _{DS} =-5V, I _D =-4A	5.5			S
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.5	-2.2	V
Diode forward voltage (note 1)	V _{SD}	I _S =-1A,V _{GS} =0V			-1	V
Dynamic characteristics (note 2)						
Input capacitance	C _{iss}	V _{DS} =-15V,V _{GS} =0V,f =1MHz		700		pF
Output capacitance	C _{oss}			120		pF
Reverse transfer capacitance	C _{rss}			75		pF
Switching Characteristics (note 2)						
Turn-on delay time	t _{d(on)}	V _{GS} =-10V,V _{DS} =-15V, R _L =3.6Ω,R _{GEN} =3Ω		8.6		ns
Turn-on rise time	t _r			5.0		ns
Turn-off delay time	t _{d(off)}			28.2		ns
Turn-off fall time	t _f			13.5		ns

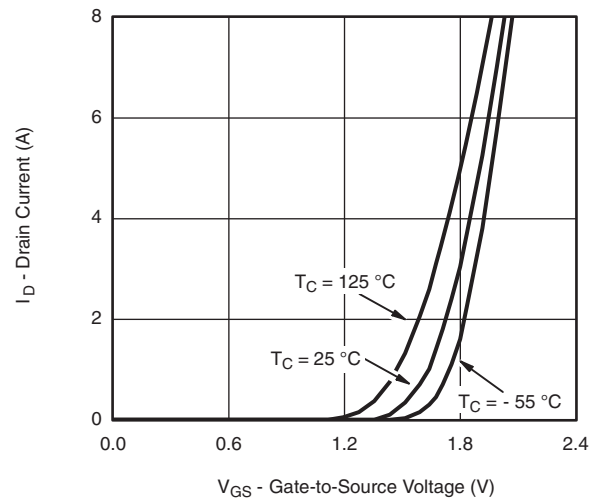
Notes:

1. Pulse test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
2. These parameters have no way to verify.

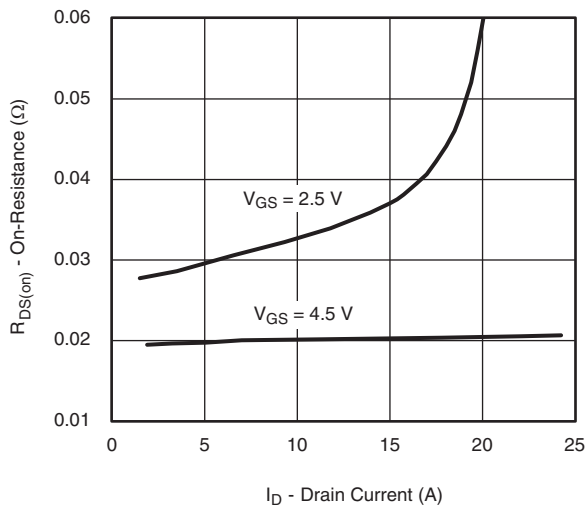
TYPICAL TRANSIENT CHARACTERISTICS



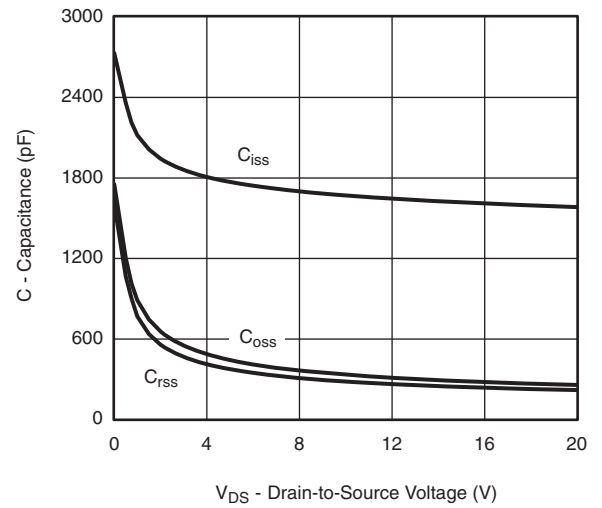
Output Characteristics



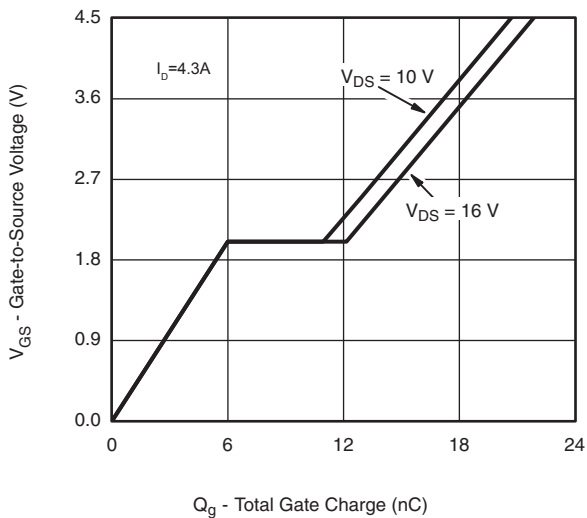
Transfer Characteristics



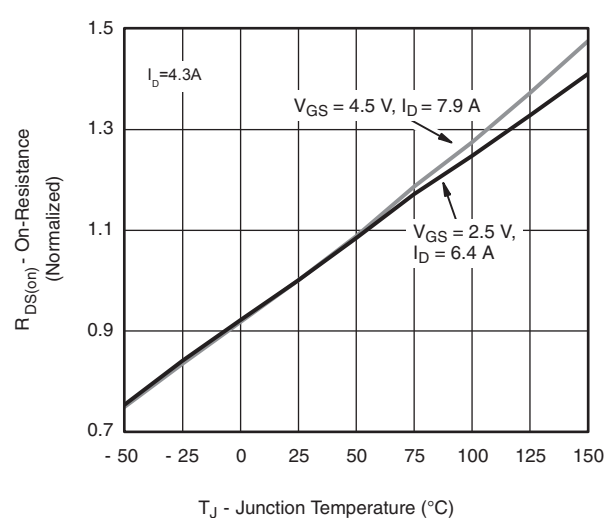
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

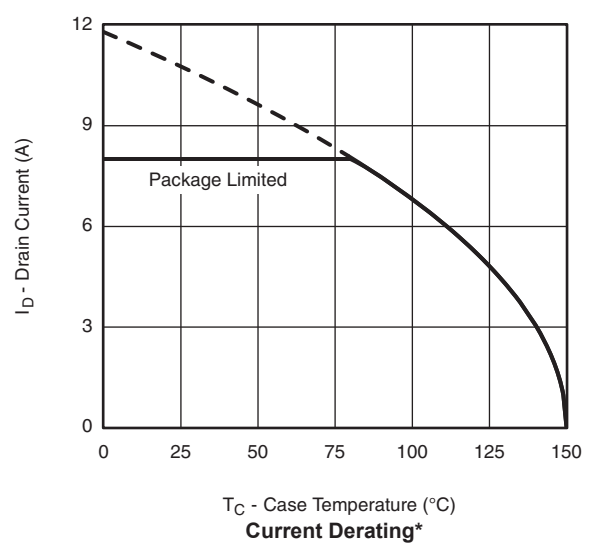
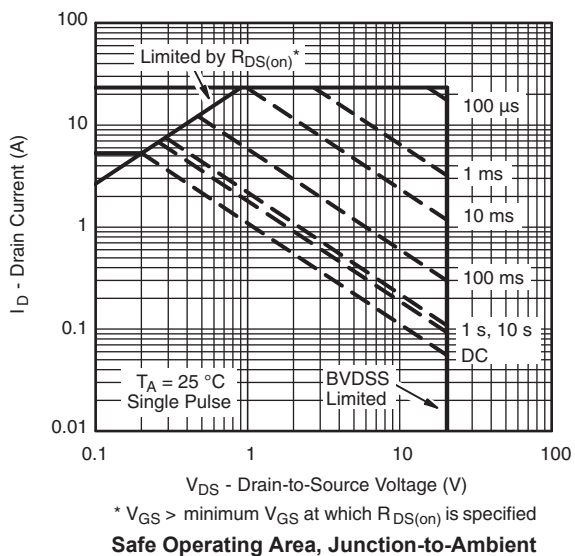
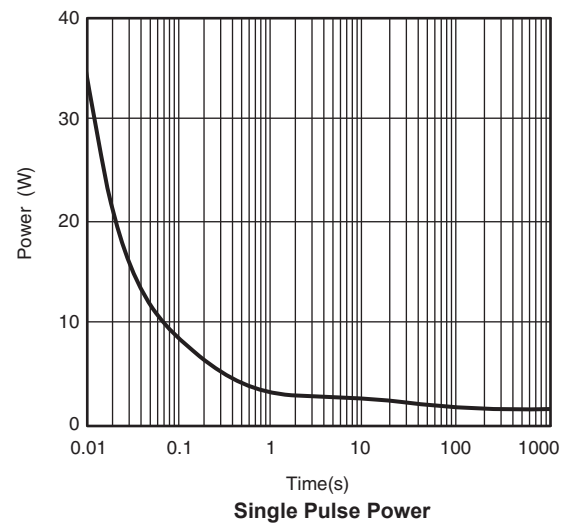
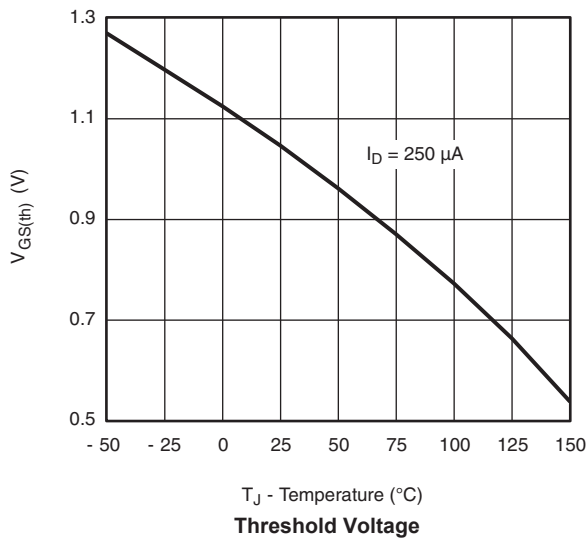
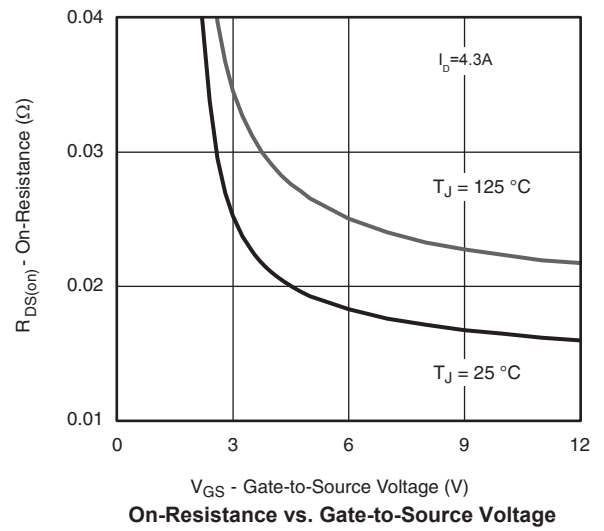
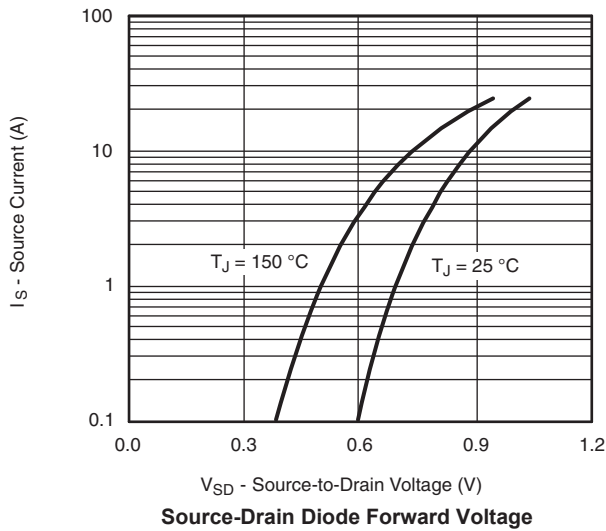


Gate Charge

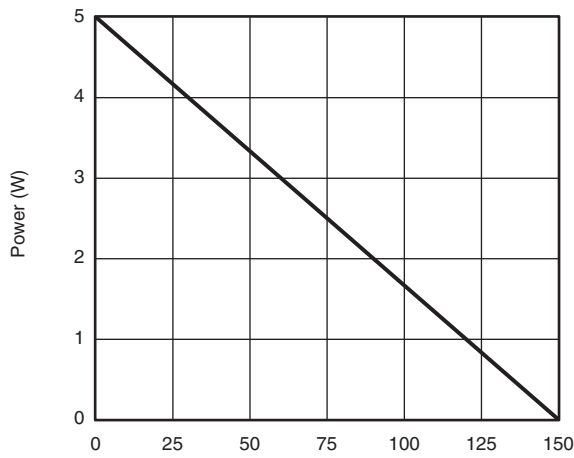


On-Resistance vs. Junction Temperature

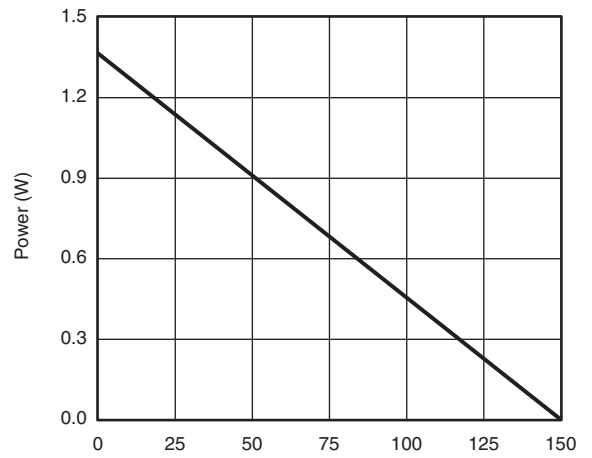
TYPICAL TRANSIENT CHARACTERISTICS



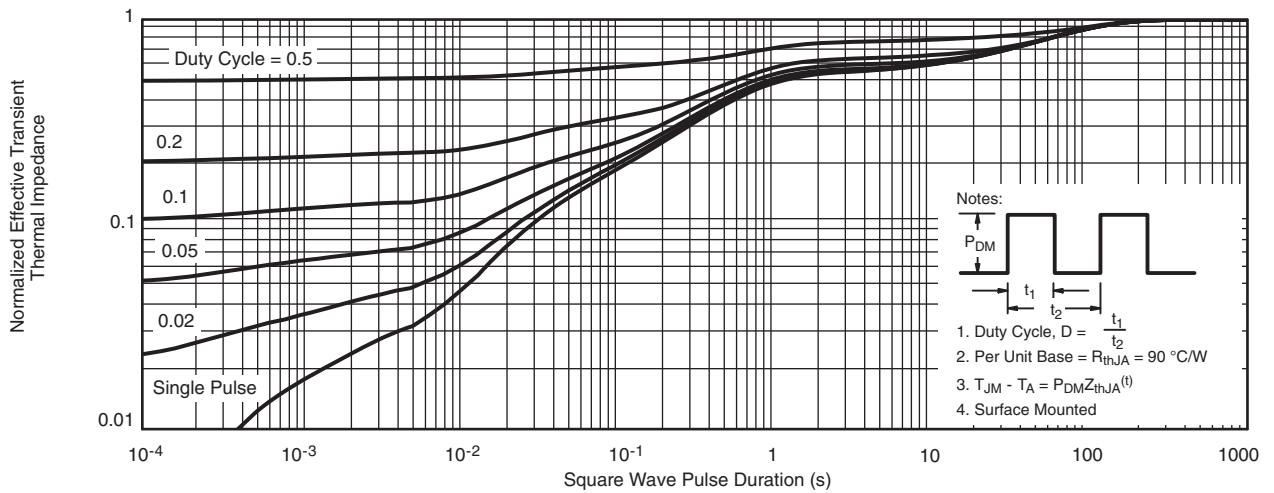
TYPICAL TRANSIENT CHARACTERISTICS



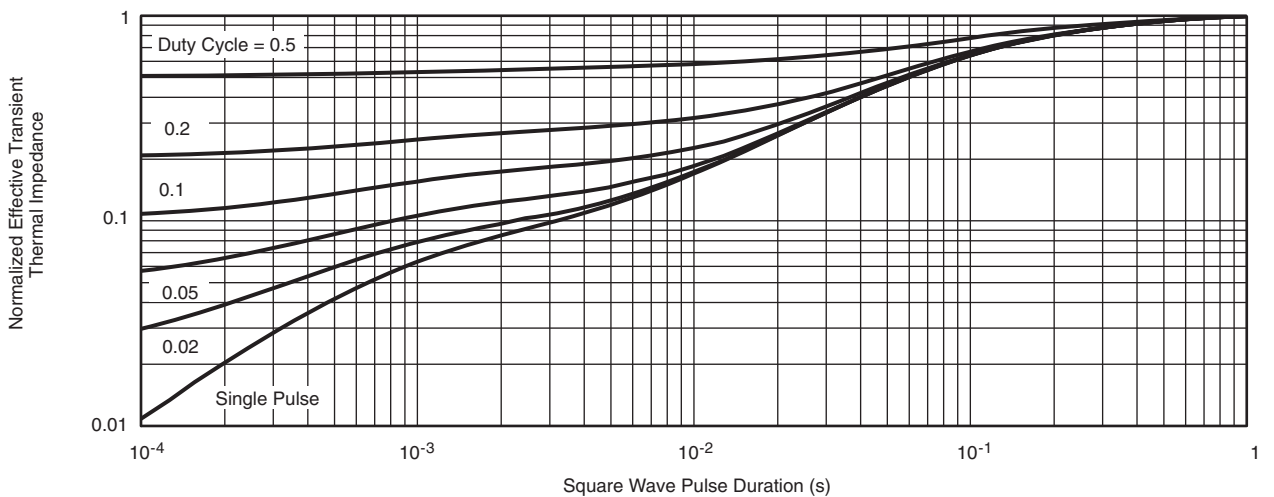
T_C - Case Temperature ($^{\circ}\text{C}$)
Power Derating, Junction-to-Foot



T_A - Ambient Temperature ($^{\circ}\text{C}$)
Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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