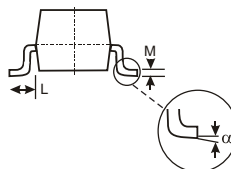
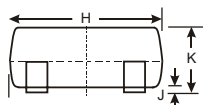
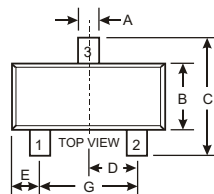


Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation,making device design easy
- Marking Code:04

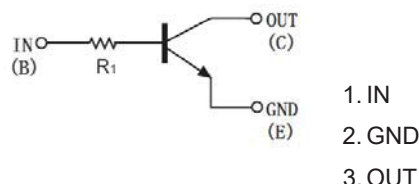


SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°
All Dimensions in mm		

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Limits	Unit
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current-Continuous	100	mA
P_D	Power Dissipation	200	mW
T_j	Junction Temperature	150	$^\circ\text{C}$

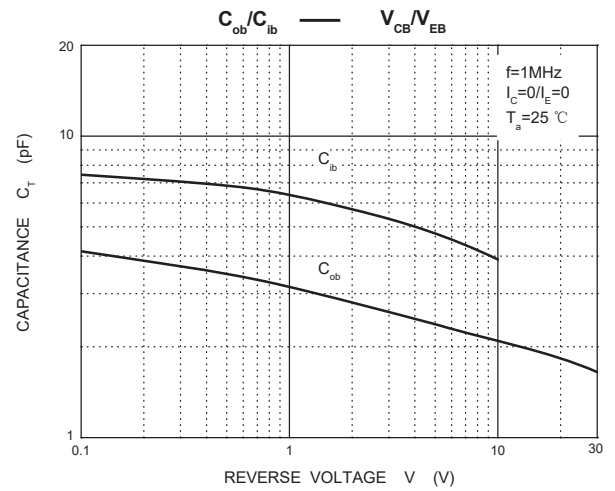
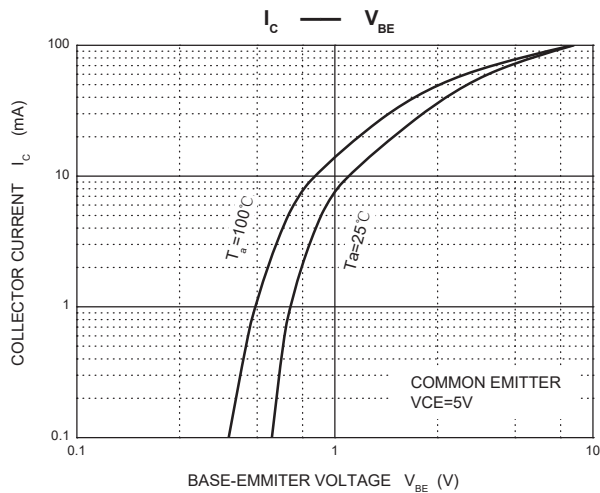
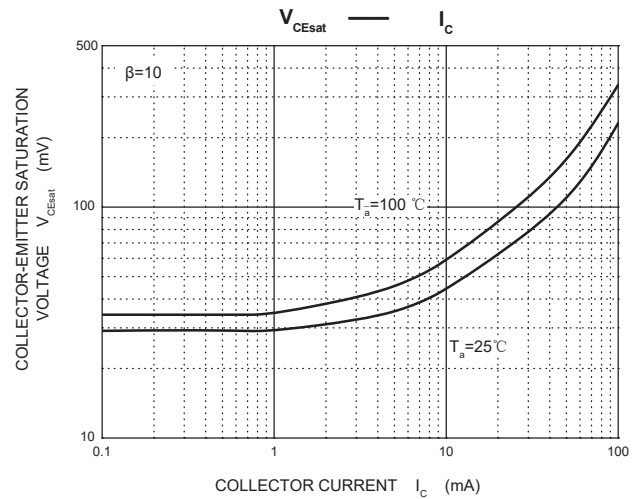
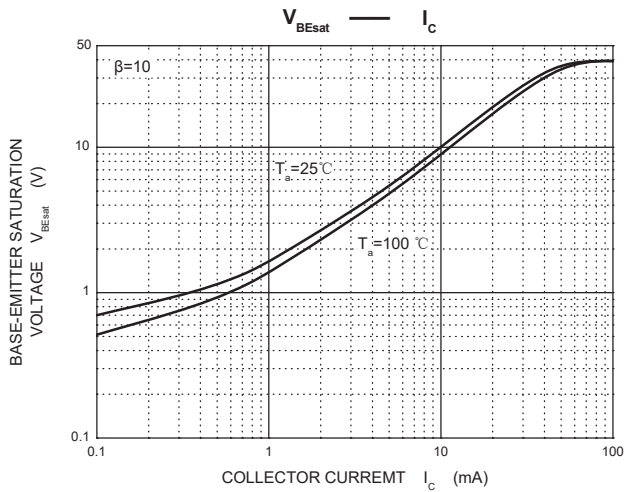
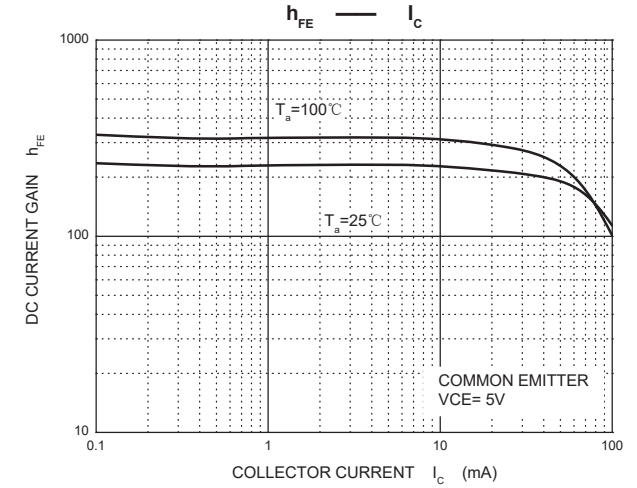
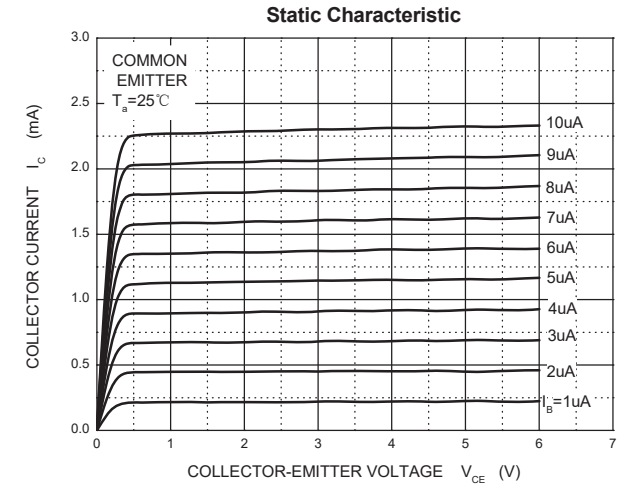
Equivalent Circuit



Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=50\mu\text{A}, I_E=0$	50			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=1\text{mA}, I_B=0$	50			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=50\mu\text{A}, I_C=0$	5			V
Collector cut-off current	I_{CBO}	$V_{CB}=50\text{V}, I_E=0$			0.5	μA
Emitter cut-off current	I_{EBO}	$V_{EB}=4\text{V}, I_C=0$			0.5	μA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=10\text{mA}, I_B=1\text{mA}$			0.3	V
DC current gain	h_{FE}	$V_{CE}=5\text{V}, I_C=1\text{mA}$	100	300	600	
Input resistor	R_1		7	10	13	k Ω
Transition frequency	f_T	$V_{CE}=10\text{V}, I_E=-5\text{mA}, f=100\text{MHz}$		250		MHz

TYPICAL TRANSIENT CHARACTERISTICS



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