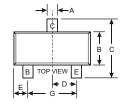
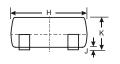


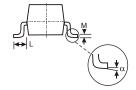
PNP SILICON EPITAXIAL PLANAR DIGITAL TRANSISTOR

Features

- With built-in bias resistors.
- Simplify circuit design.
- Reduce a quantity of parts and manufacturing process.
- Marking Code:E32



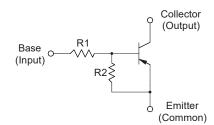




SOT-23							
Dim	Min	Max					
Α	0.37	0.51					
В	1.20	1.40					
С	2.30	2.50					
D	0.89	1.03					
E	0.45	0.60					
G	1.78	2.05					
Н	2.80	3.00					
J	0.013	0.10					
K	0.903	1.10					
L	0.45	0.61					
M	0.085	0.180					
α	0°	8°					
All Dimensions in mm							

Maximum Ratings @ T_A = 25°C unless otherwise specified

Parameter	Symbol	Value	Unit
Supply Voltage	V cc -50		V
Input Voltage	V _{IN}	-12 ∼ +5	٧
Output Current	lo	-100	mA
Power Dissipation	P _D	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature	T _{stg}	- 55 to + 150	°C



Electrical Characteristics @ T_A = 25°C unless otherwise specified

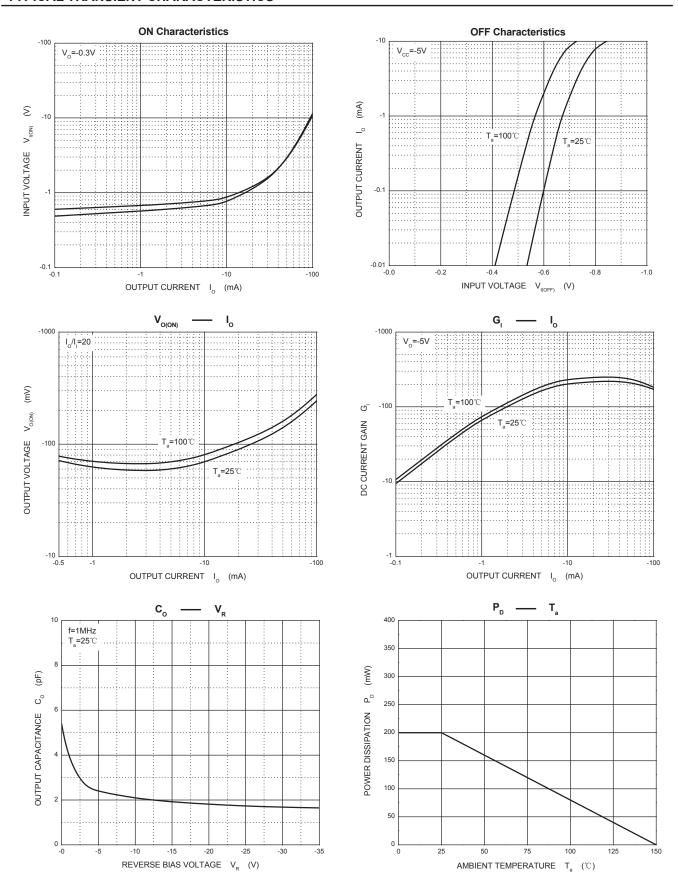
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Input voltage	$V_{I(off)}$	V _{CC} =-5V,I _O =-100μA	-0.5			V
	$V_{I(on)}$	Vo=-0.3V,lo=-5mA			-1.1	V
Output voltage	$V_{O(on)}$	Io/I:=-5mA/-0.25mA			-0.3	V
Input current	l _l	V _I =-5V			-3.6	mA
Output current	I _{O(off)}	V _{CC} =-50V,V _I =0			-0.5	μA
DC current gain	Gı	Vo=-5V,lo=-10mA	80			
Input resistance	R ₁		1.54	2.2	2.86	kΩ
Resistance ratio	R ₂ /R ₁		17	21	26	
Transition frequency	f _T	V _O =-10V,I _O =-5mA,f=100MHz		250		MHz



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PNP SILICON EPITAXIAL PLANAR DIGITAL TRANSISTOR

TYPICAL TRANSIENT CHARACTERISTICS





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PNP SILICON EPITAXIAL PLANAR DIGITAL TRANSISTOR

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