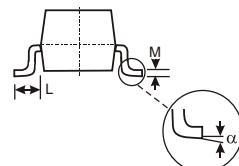
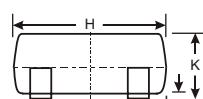
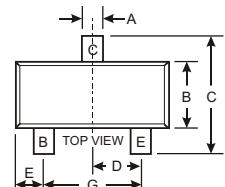


Features

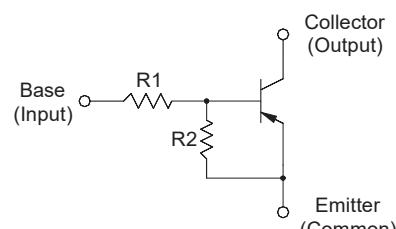
- With built-in bias resistors.
- Simplify circuit design.
- Reduce a quantity of parts and manufacturing process.
- Marking Code:16



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
α	0°	8°
All Dimensions in mm		

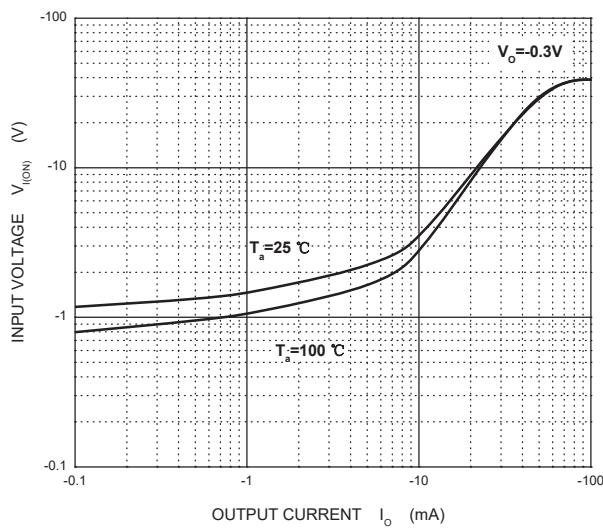
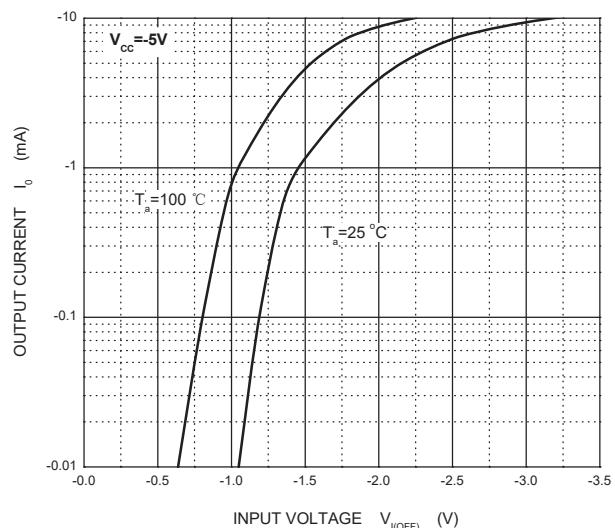
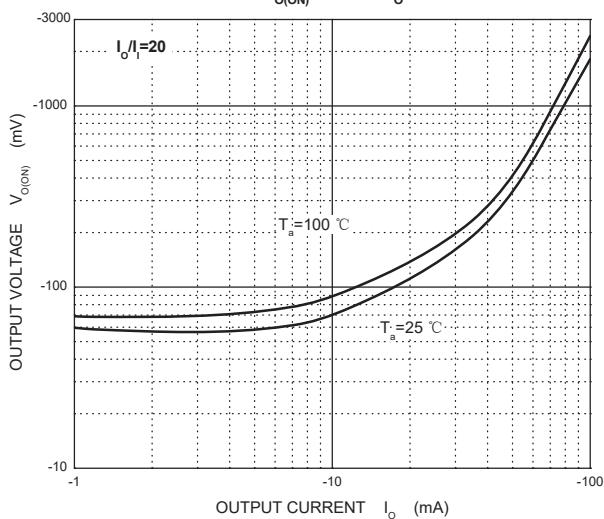
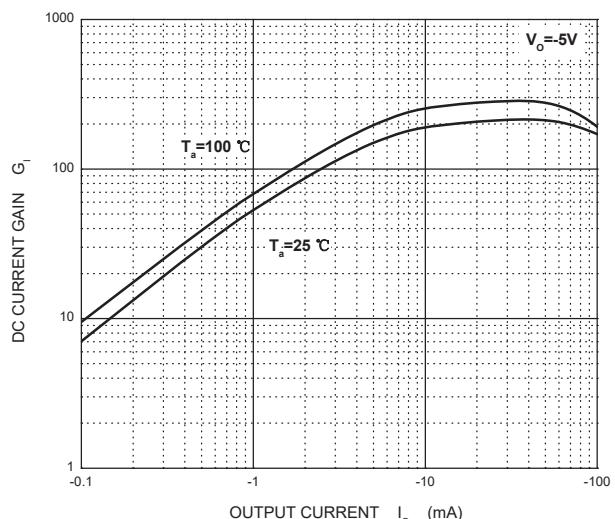
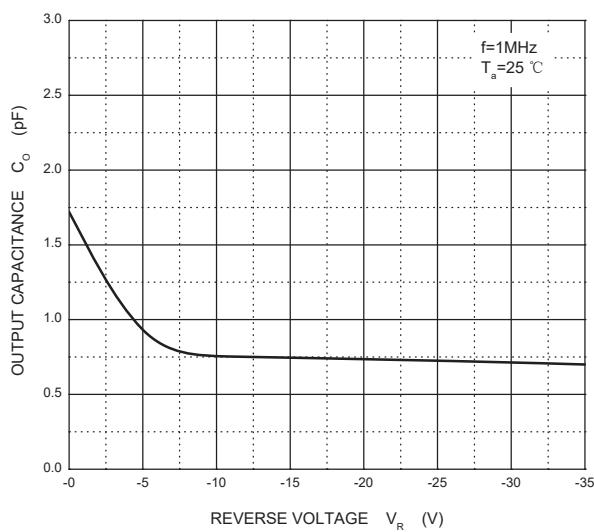
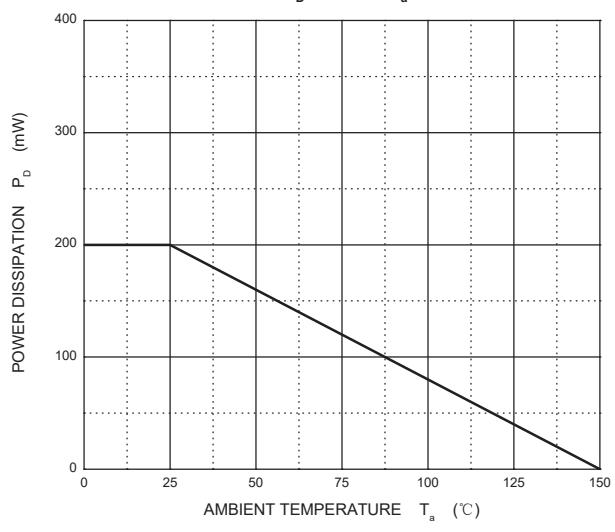
Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	-50	V
Input Voltage	V_{IN}	-40~+10	V
Output Current	I_O	-30	V
Peak Collector Current	I_{CM}	-100	mA
Power Dissipation	P_D	200	mW
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55 to +150	°C



Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	$V_{I(off)}$	$V_{CC}=-5V, I_O=-100\mu\text{A}$	-0.5			V
	$V_{I(on)}$	$V_O=-0.3V, I_O=-2\text{ mA}$			-3	V
Output voltage	$V_{O(on)}$	$I_O/I_L=-10\text{ mA}/-0.5\text{ mA}$			-0.3	V
Input current	I_I	$V_I=-5V$			-0.18	mA
Output current	$I_{O(off)}$	$V_{CC}=-50V, V_I=0$			-0.5	μA
DC current gain	G_I	$V_O=-5V, I_O=-5\text{ mA}$	68			
Input resistance	R_1		32.9	47	61.1	$k\Omega$
Resistance ratio	R_2/R_1		0.8	1	1.2	
Transition frequency	f_T	$V_O=-10V, I_O=-5\text{ mA}, f=100\text{ MHz}$		250		MHz

PNP SILICON EPITAXIAL PLANAR DIGITAL TRANSISTOR
TYPICAL TRANSIENT CHARACTERISTICS
ON Characteristics

OFF Characteristics

 $V_{o(\text{ON})} \text{ --- } I_o$

 $G_i \text{ --- } I_o$

 $C_o \text{ --- } V_R$

 $P_D \text{ --- } T_a$


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