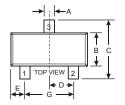
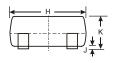


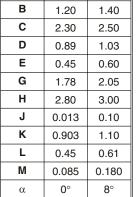
## **DUAL SURFACE MOUNT SWITCHING DIODES**

### **Features**

- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automated Insertion
- For General Purpose Switching Applications
- High Conductance
- Marking Code: A1







All Dimensions in mm

SOT-23

Min

0.37

Max

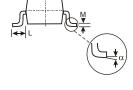
0.51

Dim

Α

# Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Unit	Value
Non-Repetitive Peak Reverse Voltage	I <sub>FM</sub>	300	mA
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	75	٧
RMS Reverse Voltage	V <sub>R(RMS)</sub>	53	V
Forward Continuous Current (Note 1)	I <sub>FM</sub>	300	mA
Non-Repetitive Peak Forward Surge Current @ t = 1.0μs @ t = 1.0s	I <sub>FSM</sub>	2.0 1.0	А
Power Dissipation (Note 1)	$P_{D}$	350	mW
Thermal Resistance Junction to Ambient Air (Note 1)	$R_{\theta JA}$	350	°C/W
Operating and Storage Temperature Range	$T_J$ , $T_{STG}$	-65 to +150	°C



Top	View
nternal S	Schematic



## Electrical Characteristics @ TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	ConditiTeston
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	75	_	_	V	$I_R = 100 \mu A$
Forward Voltage	V <sub>F</sub>		_	0.715 0.855 1.0 1.25	V	I <sub>F</sub> = 1.0mA I <sub>F</sub> = 10mA I <sub>F</sub> = 50mA I <sub>F</sub> = 150mA
Leakage Current (Note 2)	I <sub>R</sub>		_	2.5 50	μ <b>Α</b> μ <b>Α</b>	V <sub>R</sub> = 75V V <sub>R</sub> = 75V, T <sub>J</sub> = 150°C
Total Capacitance	C <sub>T</sub>	_	_	2.0	pF	$V_R = 0$ , $f = 1.0MHz$
Reverse Recovery Time	t <sub>rr</sub>	_	_	4.0	μS	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

Notes:

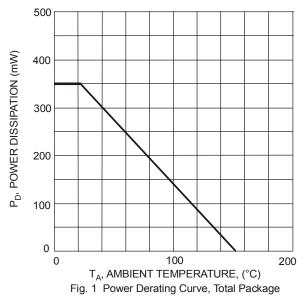
- Part mounted on FR-4 board with recommended pad layout.
  Short duration pulse test used to minimize self-heating effect.

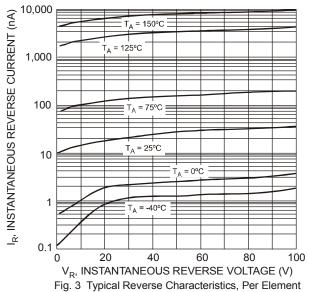


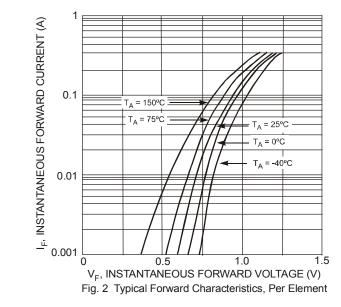


## **DUAL SURFACE MOUNT SWITCHING DIODES**

### TYPICAL TRANSIENT CHARACTERISTICS







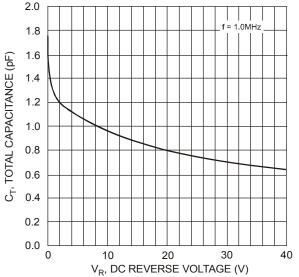


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

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### SUATUSURSTANCEANO UNTULO VISINETANA DE DIODES

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