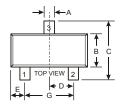
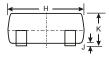


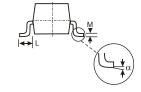
DUAL SURFACE MOUNT LOW LEAKAGE DIODES

Features

- Surface Mount Package Ideally Suited for Automated Insertion
- Very Low Leakage Current







SOT-23						
Dim	Min	Max				
Α	0.37	0.51				
В	1.20	1.40				
С	2.30	2.50				
D	0.89	1.03				
Е	0.45	0.60				
G	1.78	2.05				
Н	2.80	3.00				
J	0.013	0.10				
K	0.903	1.10				
L	0.45	0.61				
M	0.085	0.180				
α	0°	8°				
All Dimensions in mm						

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Unit	Value
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	85	V
RMS Reverse Voltage	V _{R(RMS)}	60	V
Forward Continuous Current (Note 1) Single diode Double diode	I _{FM}	160 140	mA
Repetitive Peak Forward Current (Note 1)	I _{FRM}	500	mA
Non-Repetitive Peak Forward Surge Current @ t = 1.0µs @ t = 1.0ms @ t = 1.0s	I _{FSM}	4.0 1.0 0.5	А
Power Dissipation (Note 1)	P_{D}	250	mW
Thermal Resistance Junction to Ambient Air (Note 1)	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	T_J , T_{STG}	-65 to +150	°C

Top View Internal Schematic



Electrical Characteristics @ TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	ConditiTeston
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	85	_	_	V	$I_R = 100 \mu A$
Forward Voltage	V _F		_	0.90 1.0 1.1 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Leakage Current (Note 2)	I _R	_	_	5.0 80	nA nA	$V_R = 75V$ $V_R = 75V$, $T_J = 150$ °C
Total Capacitance	C _T	_	3	_	pF	$V_R = 0$, $f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	_	3.0	μS	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

Notes:

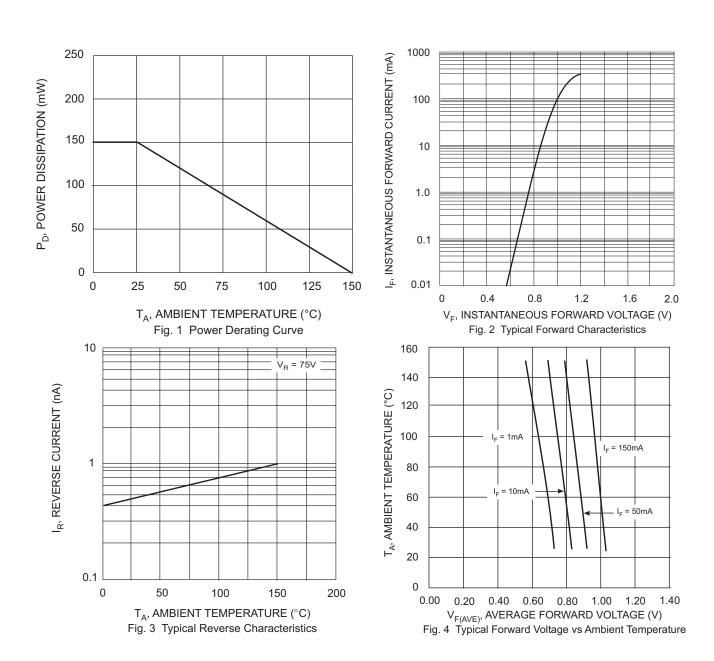
- 1. Part mounted on FR-4 board with recommended pad layout. 2. Short duration pulse test used to minimize self-heating effect.





DUAL SURFACE MOUNT LOW LEAKAGE DIODES

TYPICAL TRANSIENT CHARACTERISTICS



http://www.hc-semi.com



DUAL SURFACE MOUNT LOW LEAKAGE DIODES

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